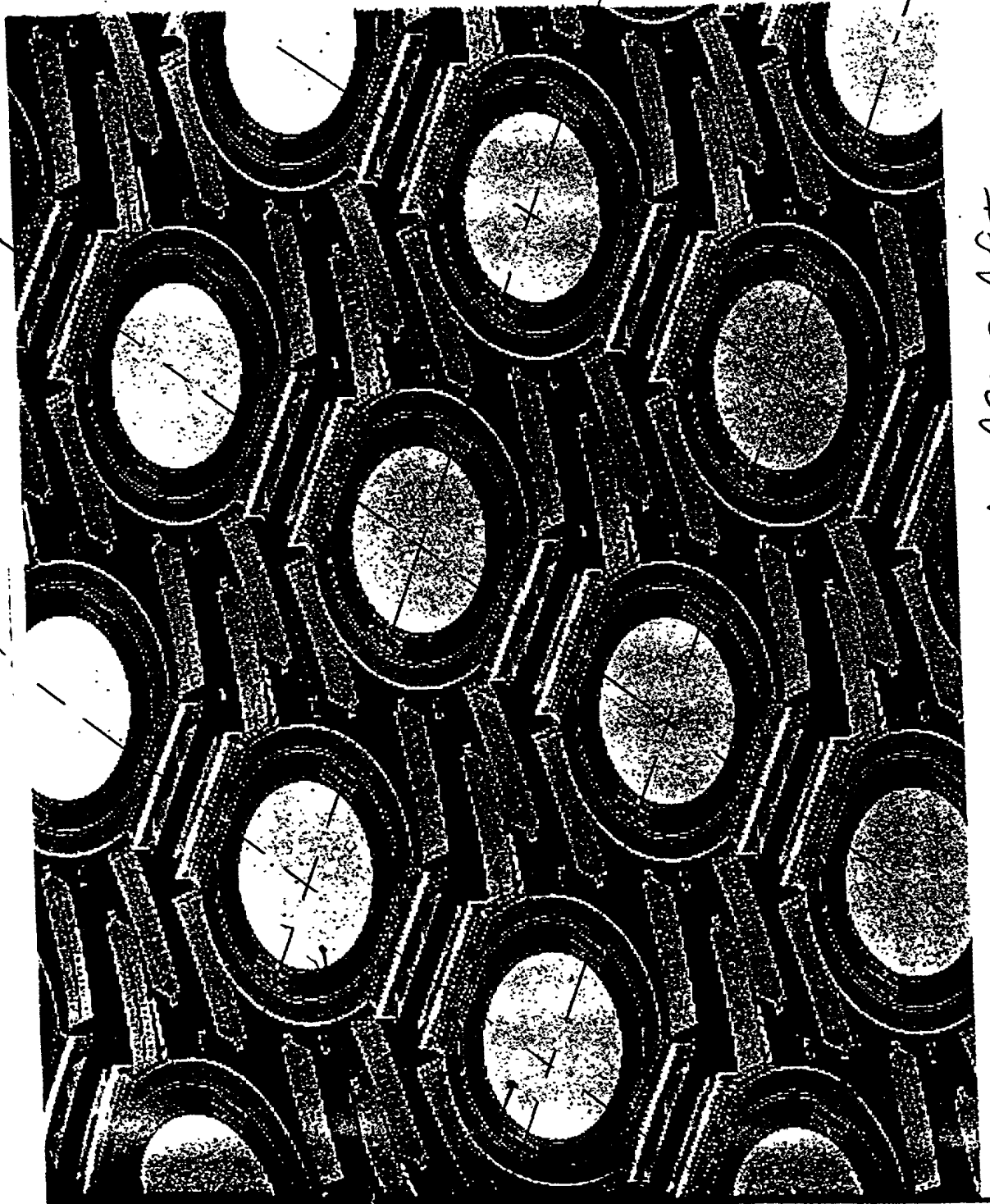


FIGURE 1  
PRIOR ART

201207055200T

1016

1016



1010

1011

1012

FIGURE 1A PRIOR ART

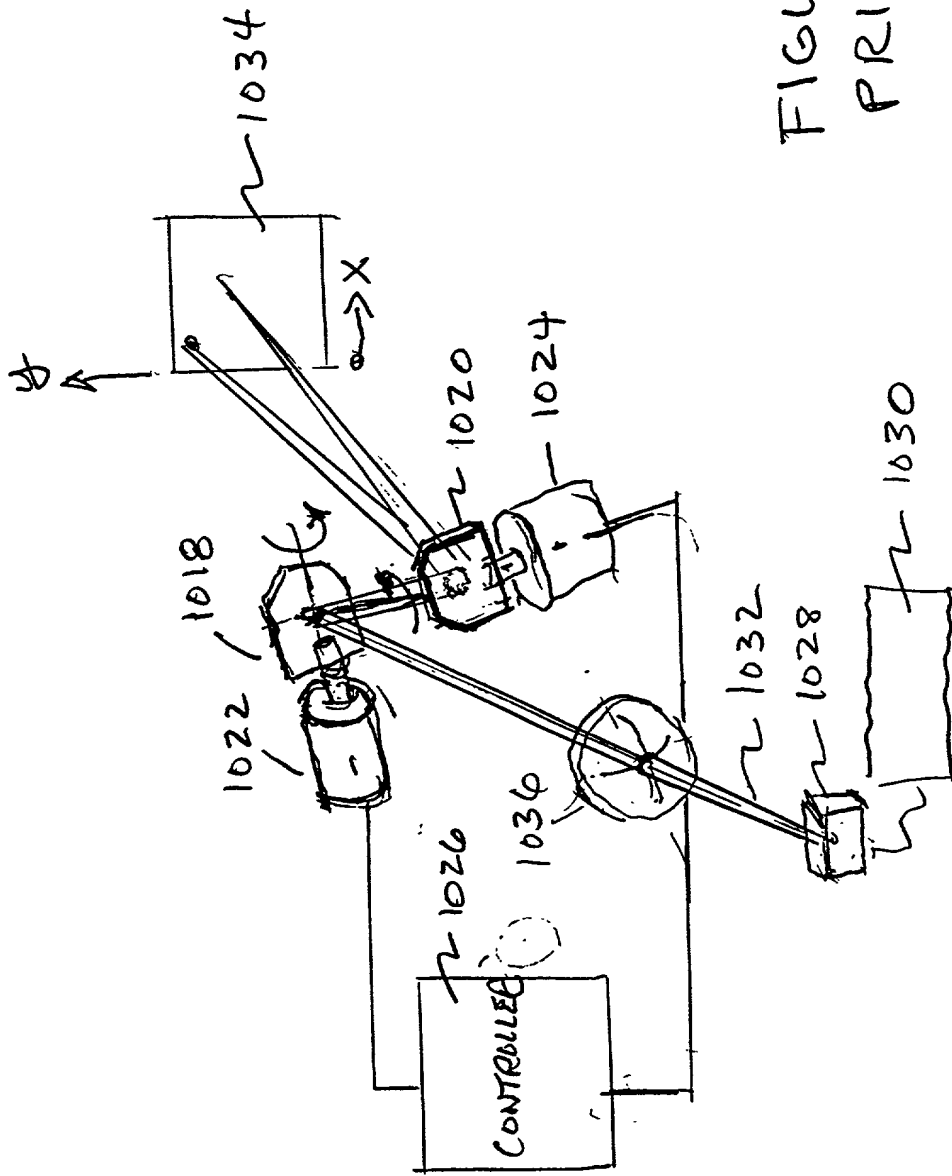
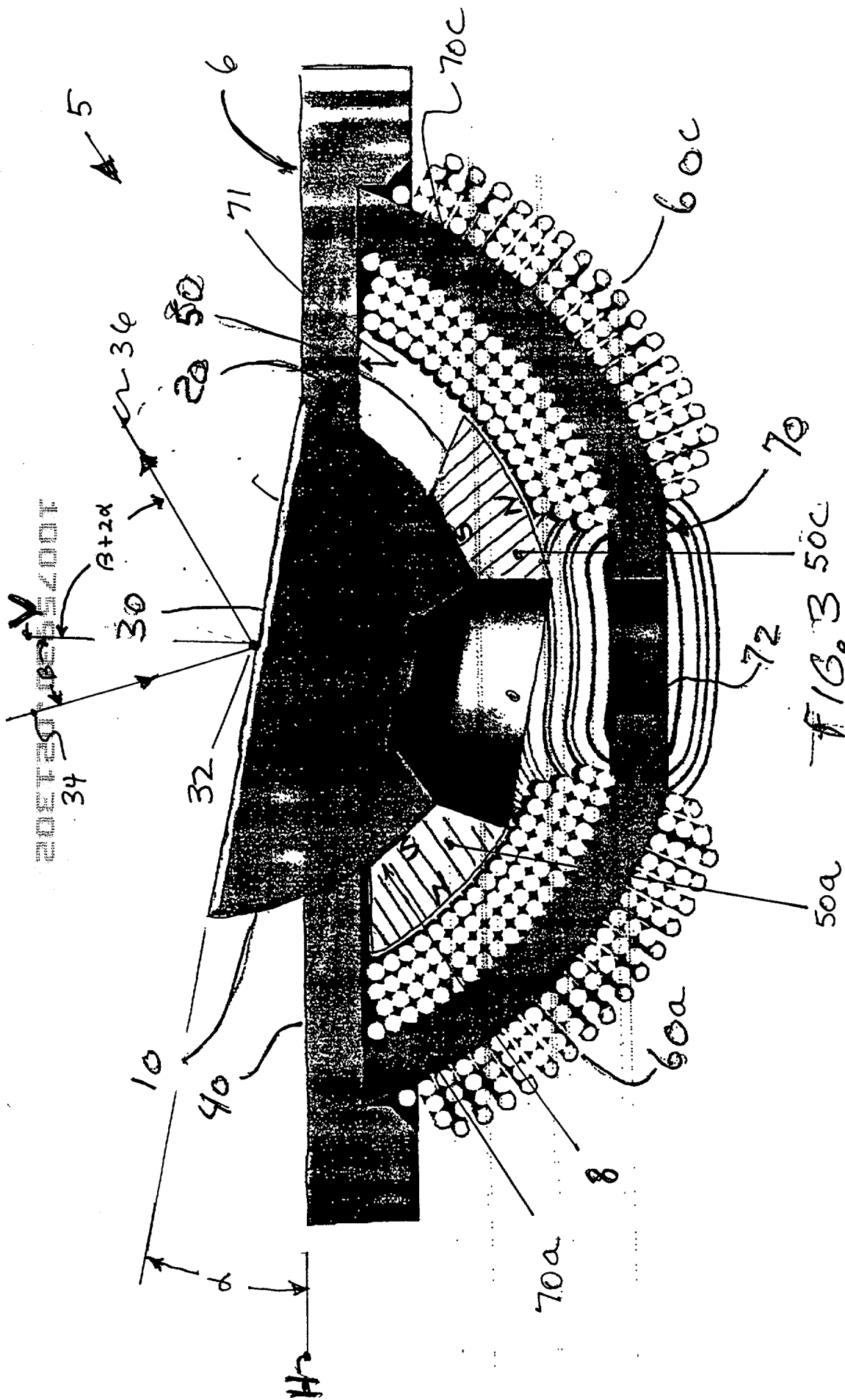
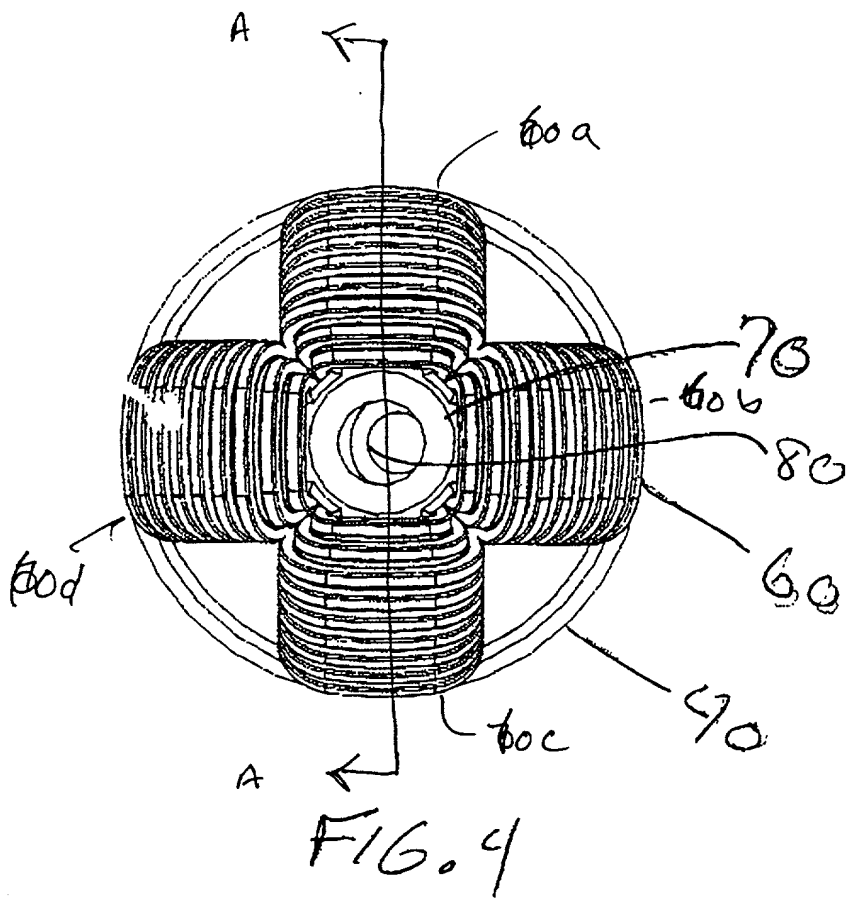


FIGURE 2  
PRIOR ART





10075930.024302





**SECRET**

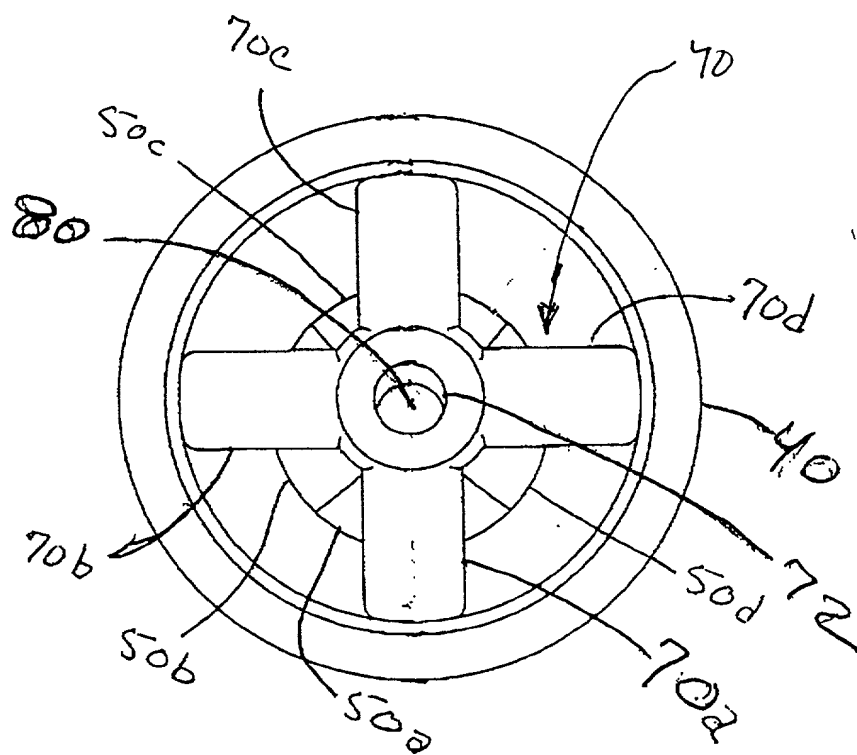


FIG. 6





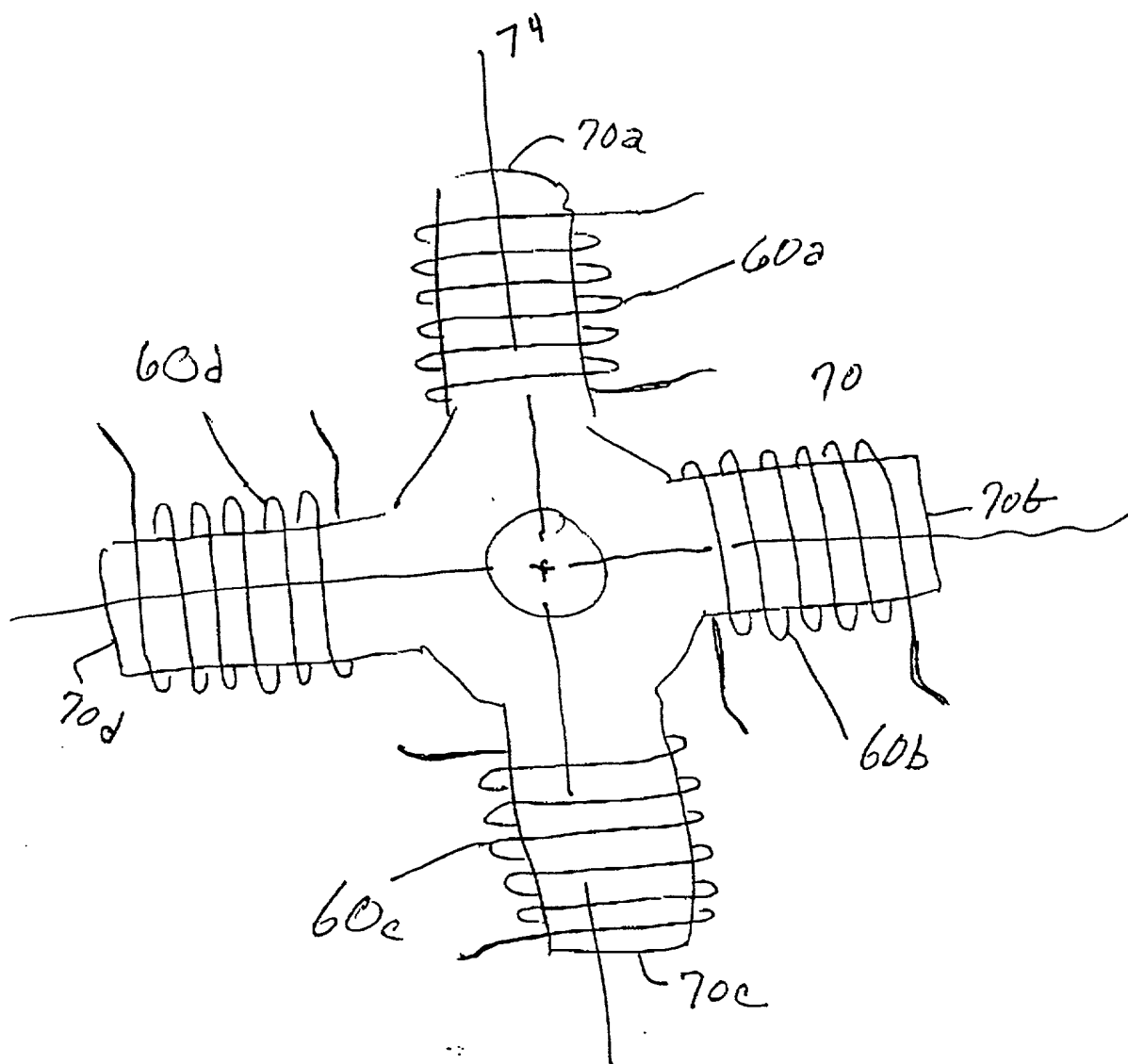
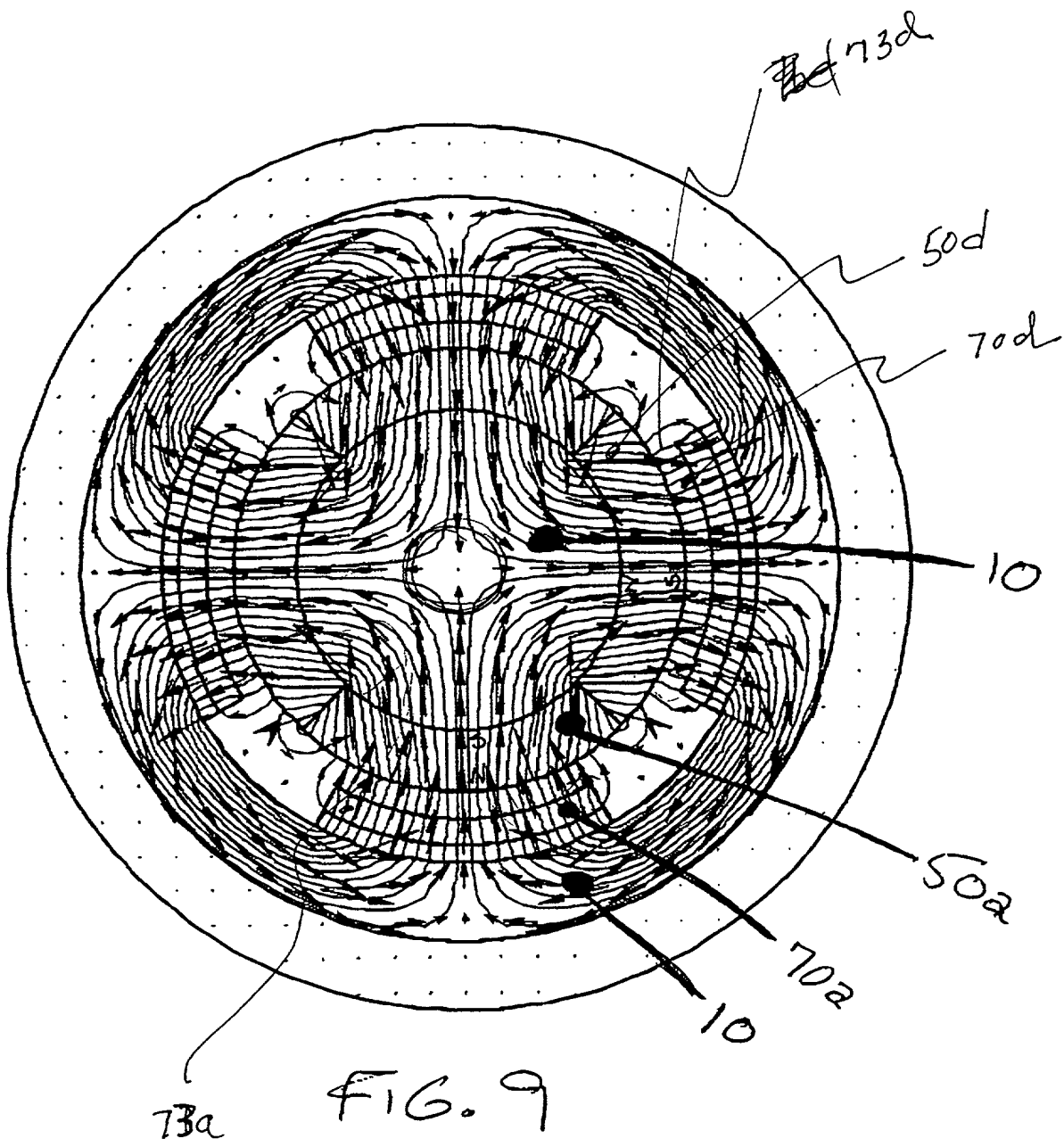


FIG. 8

10075930-021302



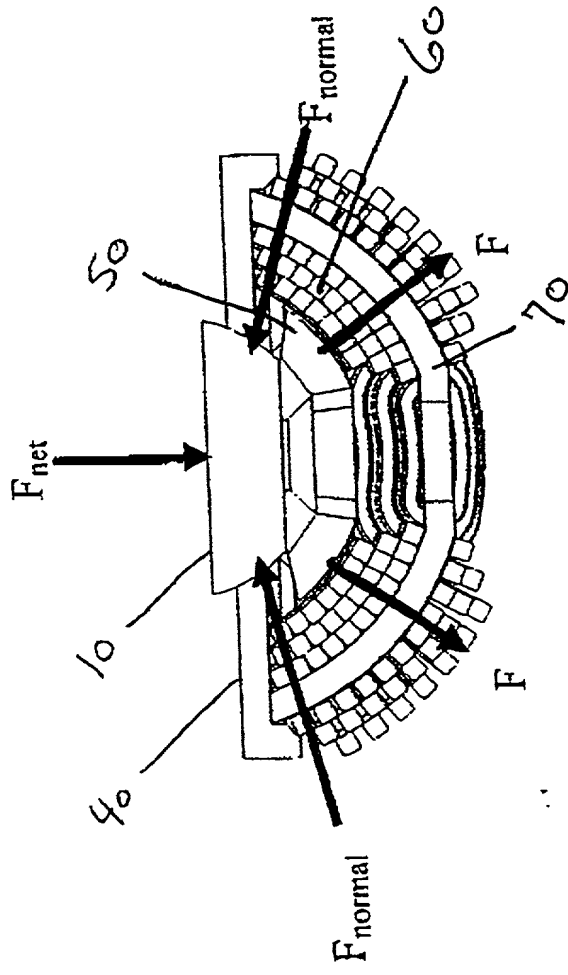


FIG. 10

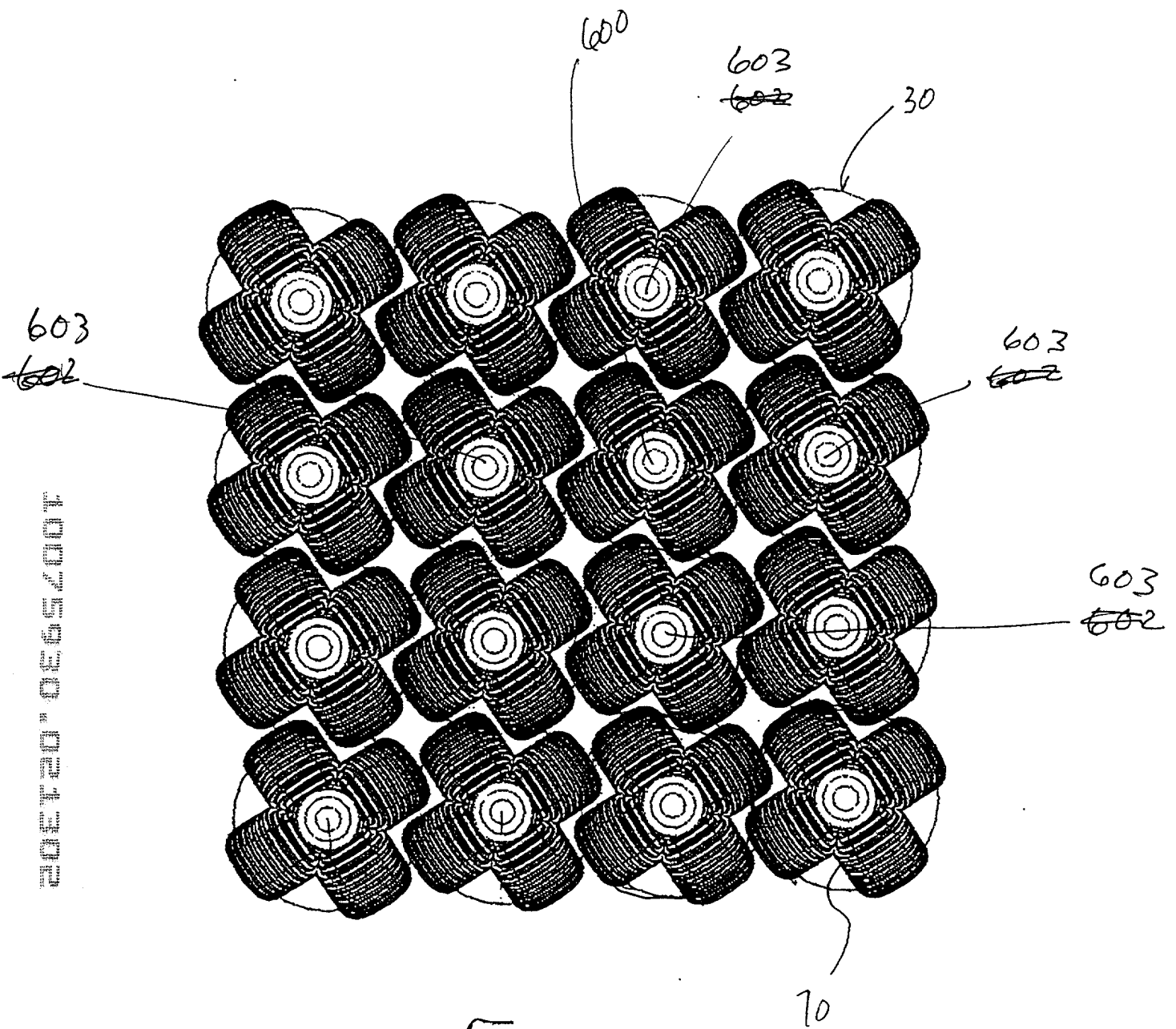
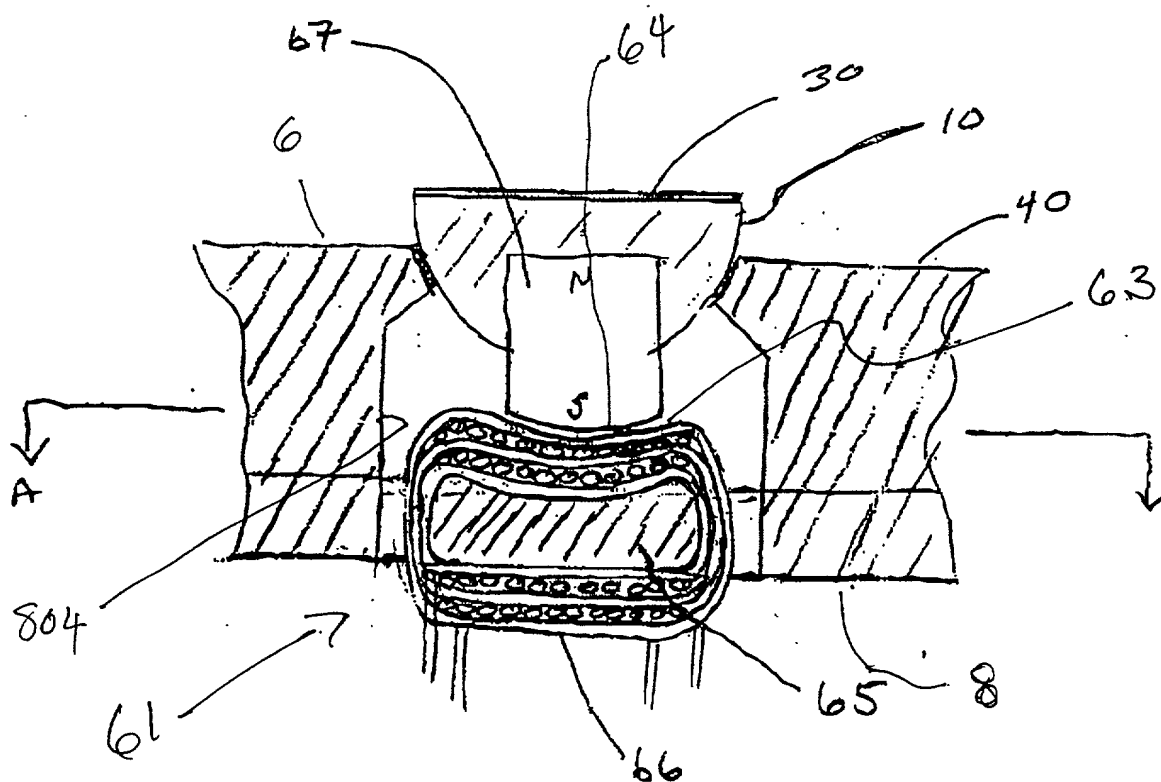
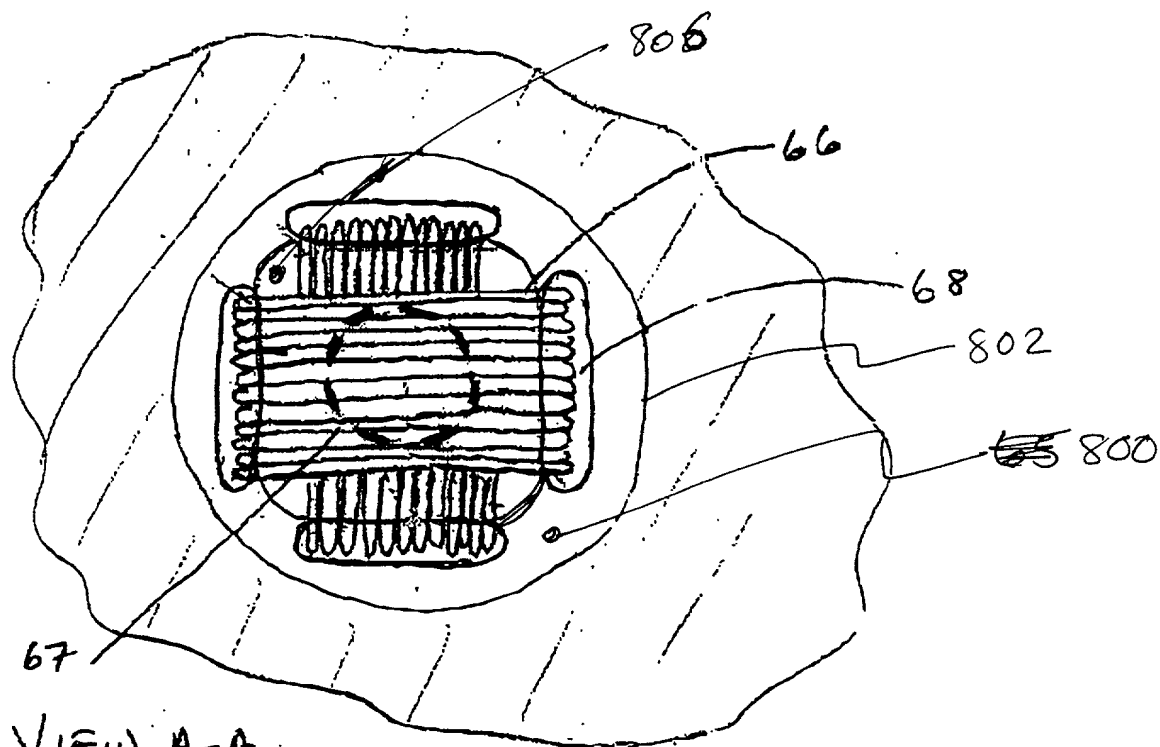


FIG. 11

10075930.021302



3501

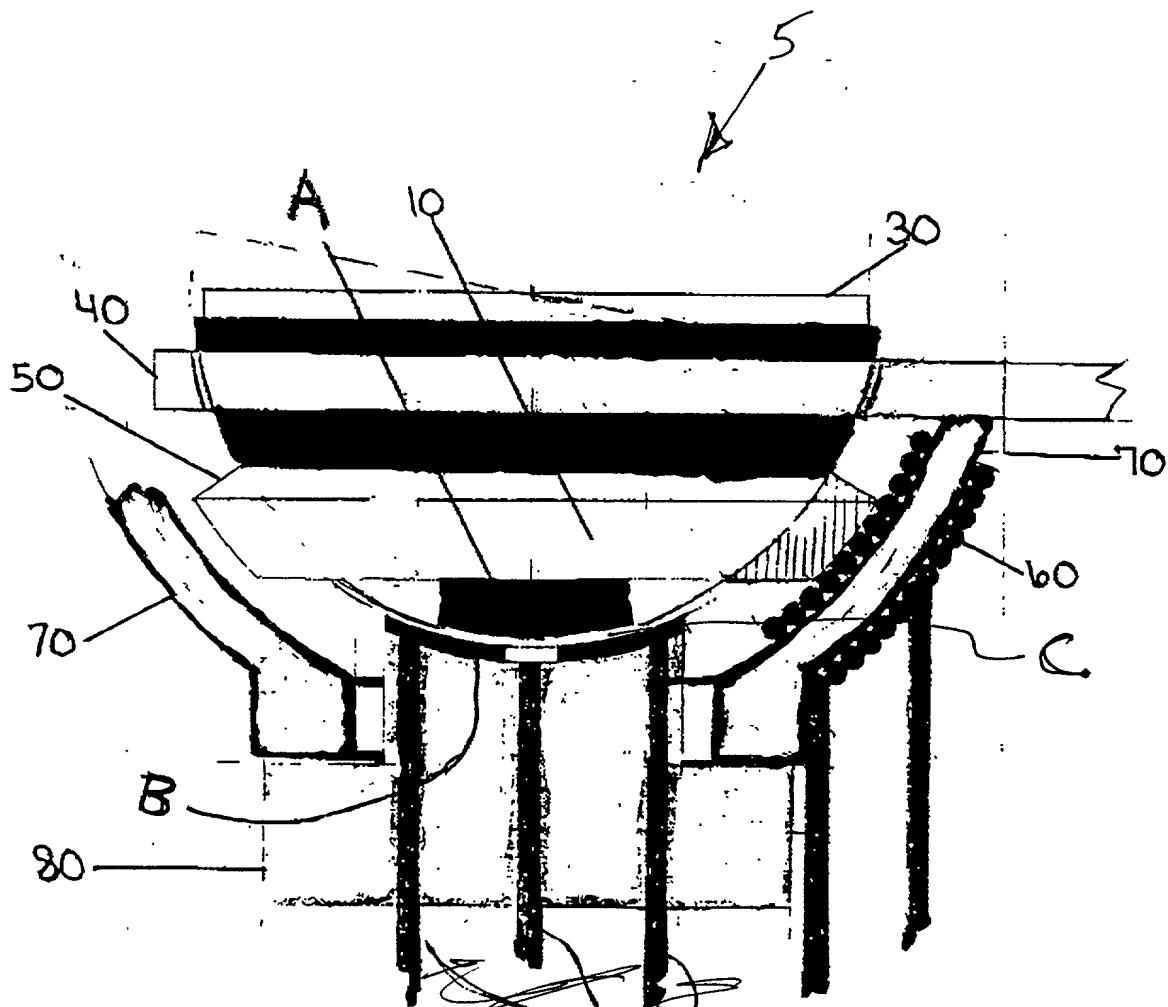
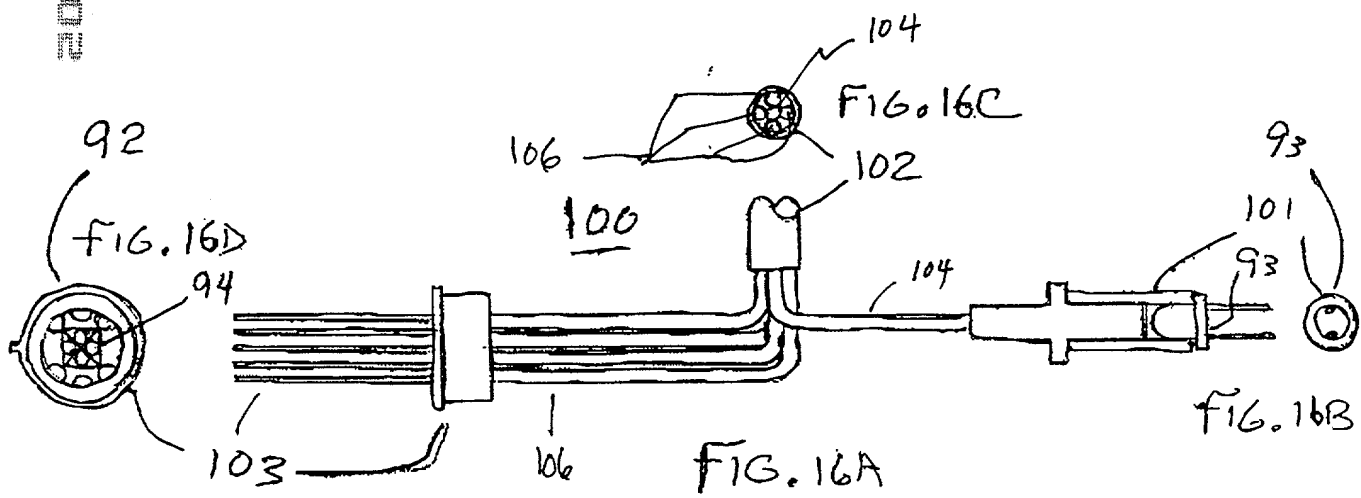
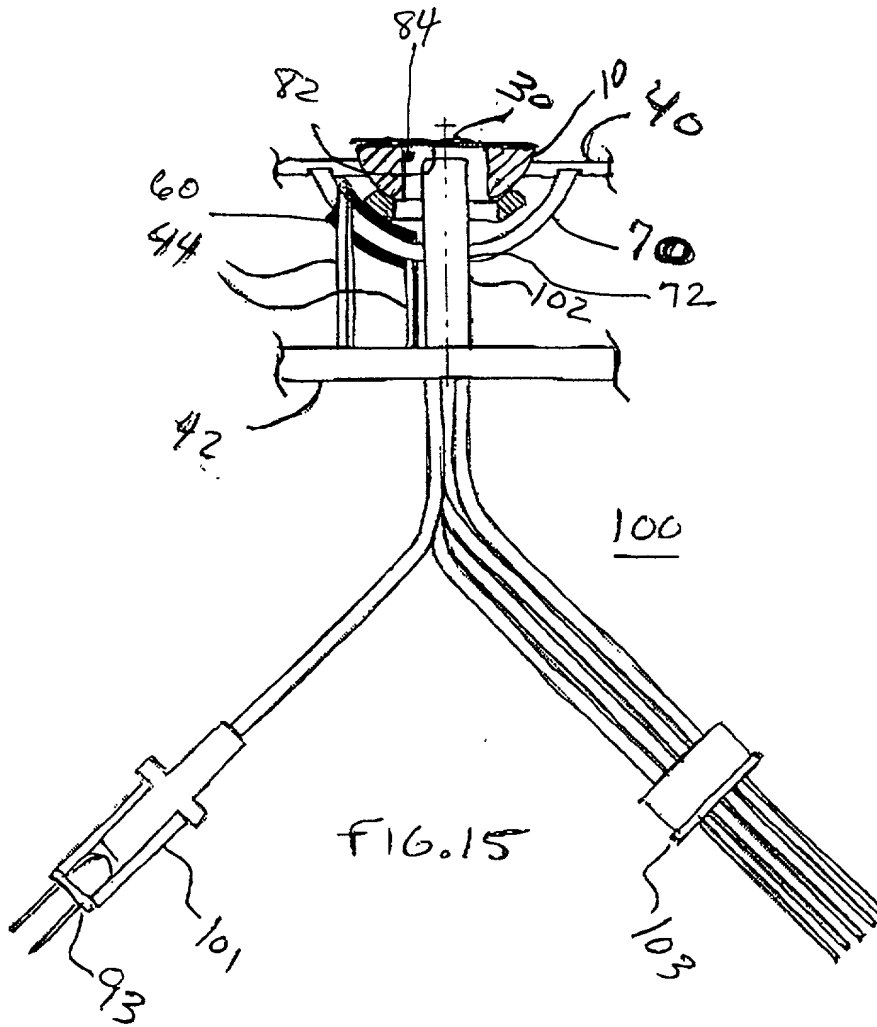


FIG. 129





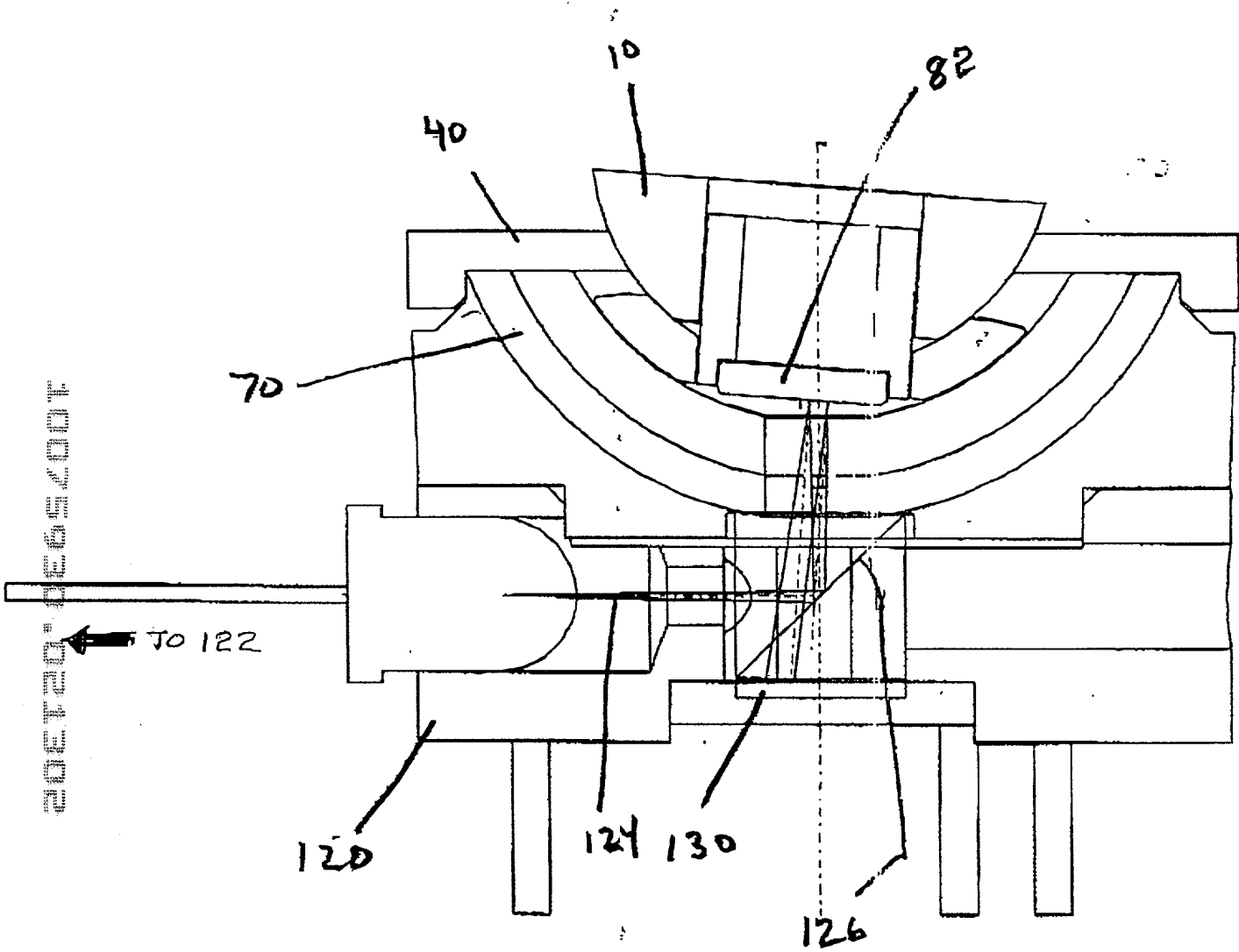


Fig 17

202

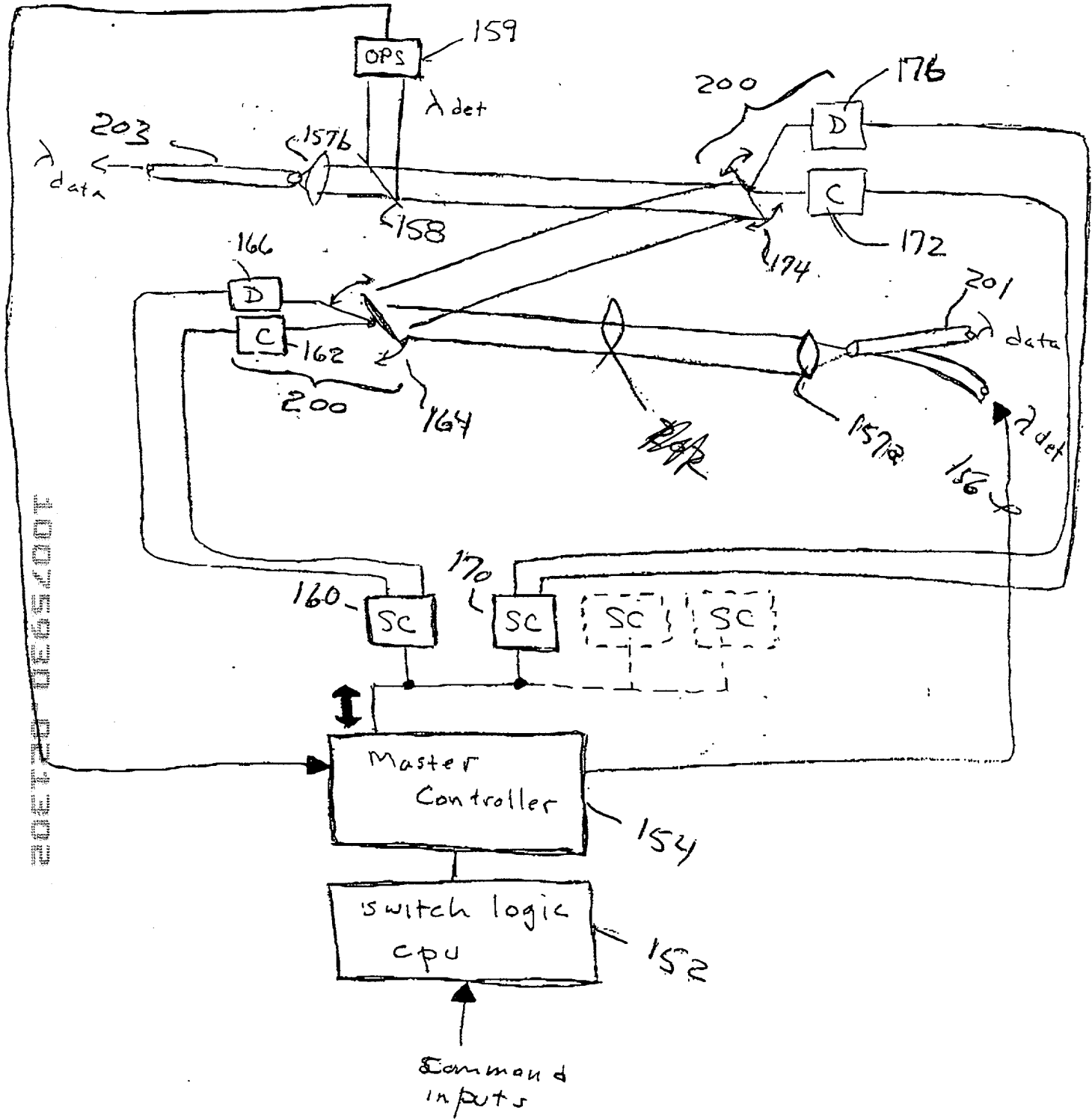


FIG. 18

The diagram illustrates a multi-channel optical communication system, labeled 302. It features a central Master Controller 154 and a switch logic CPU 152. The Master Controller 154 is connected to a series of slave units 160, 170, and 176 via a bus. Each slave unit contains a switch (SC) and a set of optical components (D and C). The system is connected to a data source 203 and a data sink 201 via optical fibers 204 and 202. The diagram also shows a data detector 156 and a data source 201. The system is labeled 302.

302

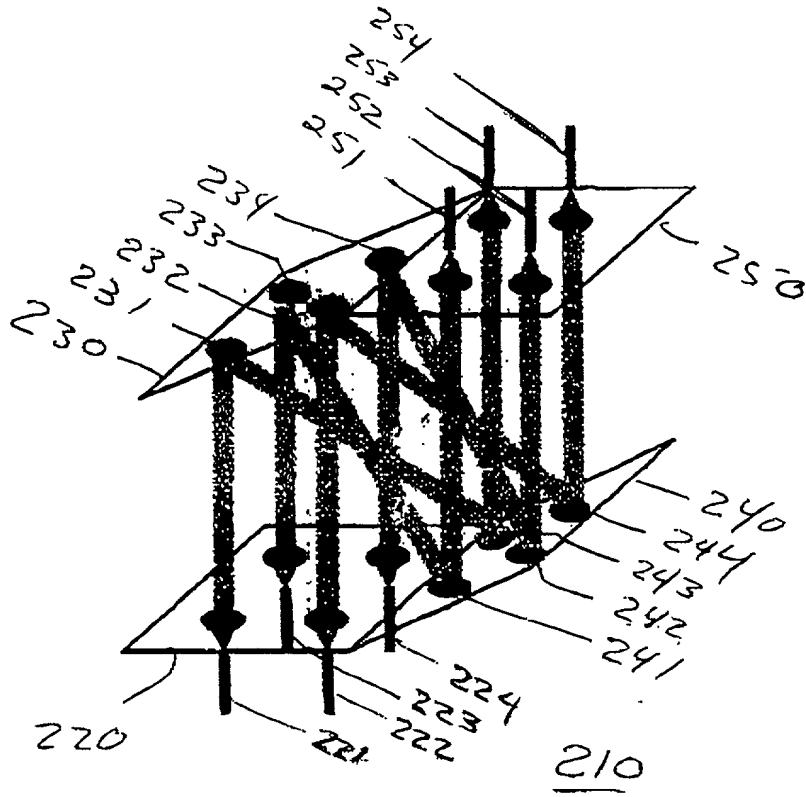
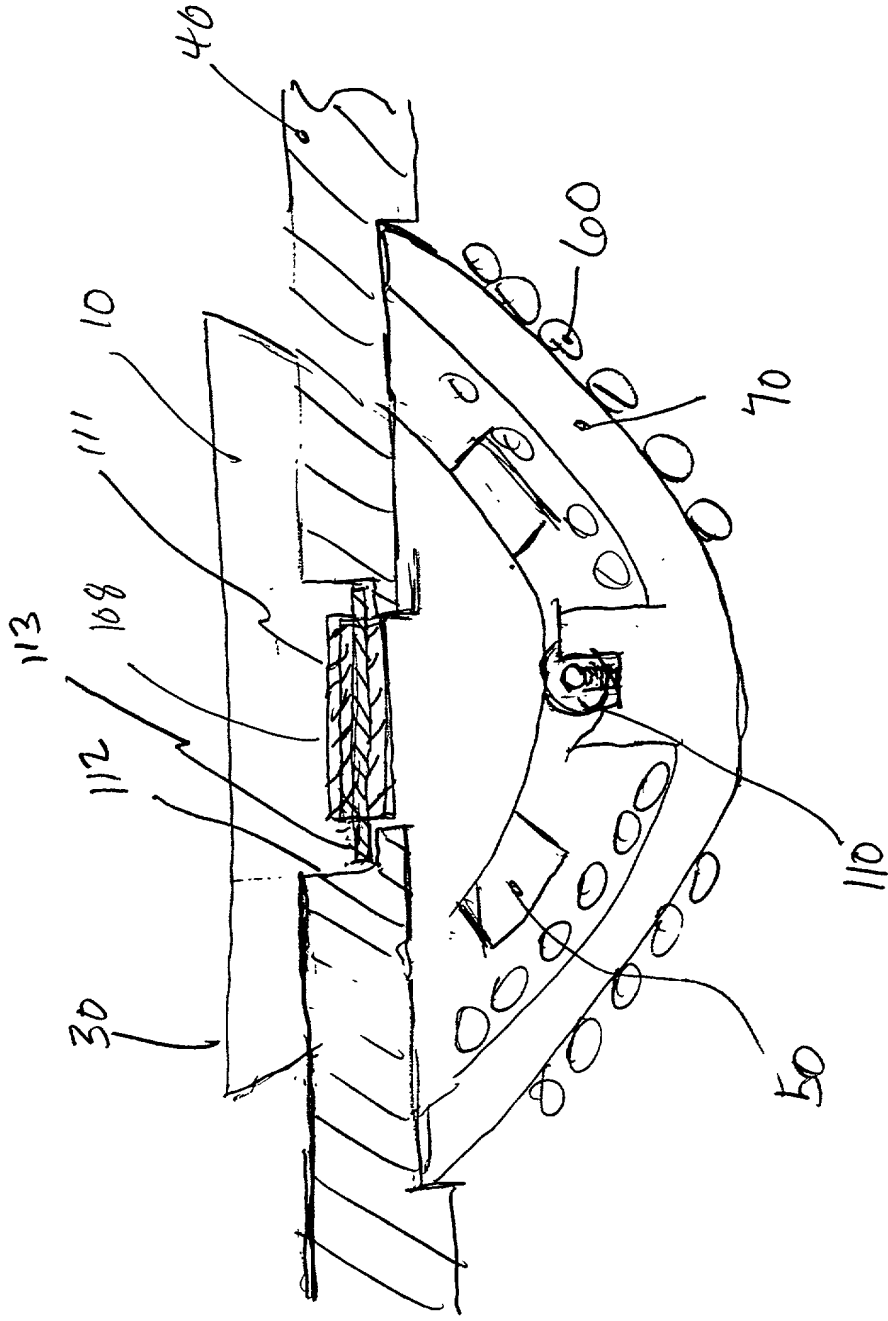


FIG. 20



400

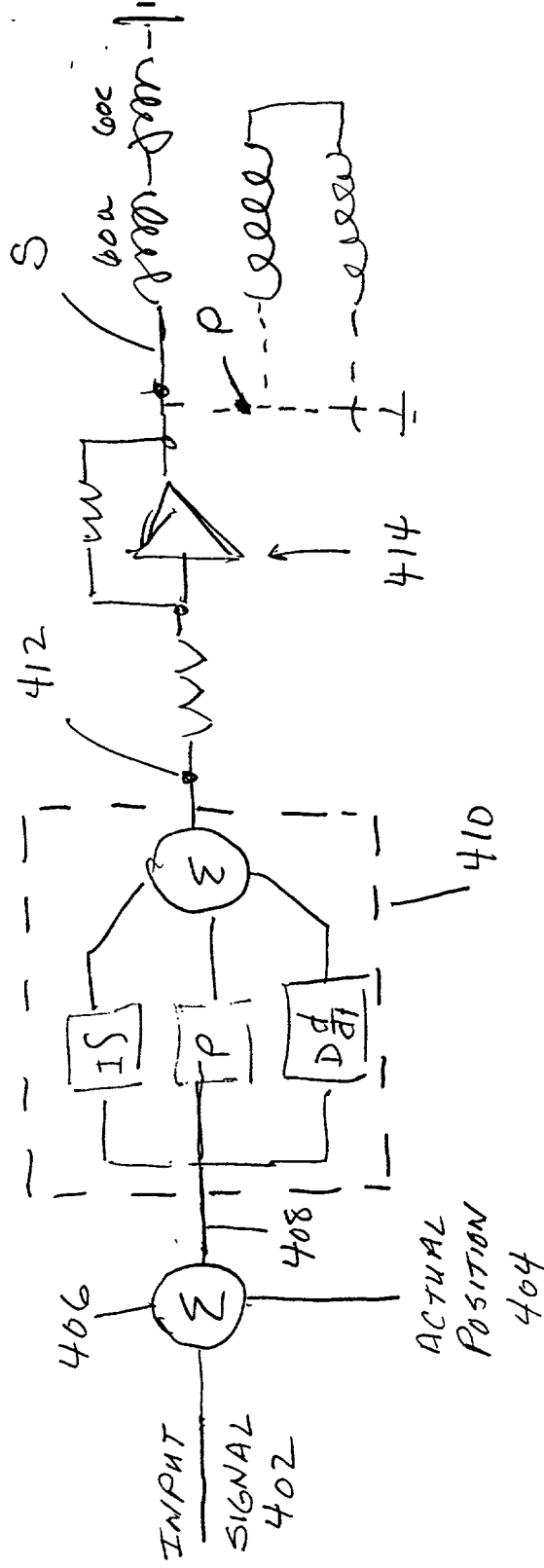


FIGURE 22

500

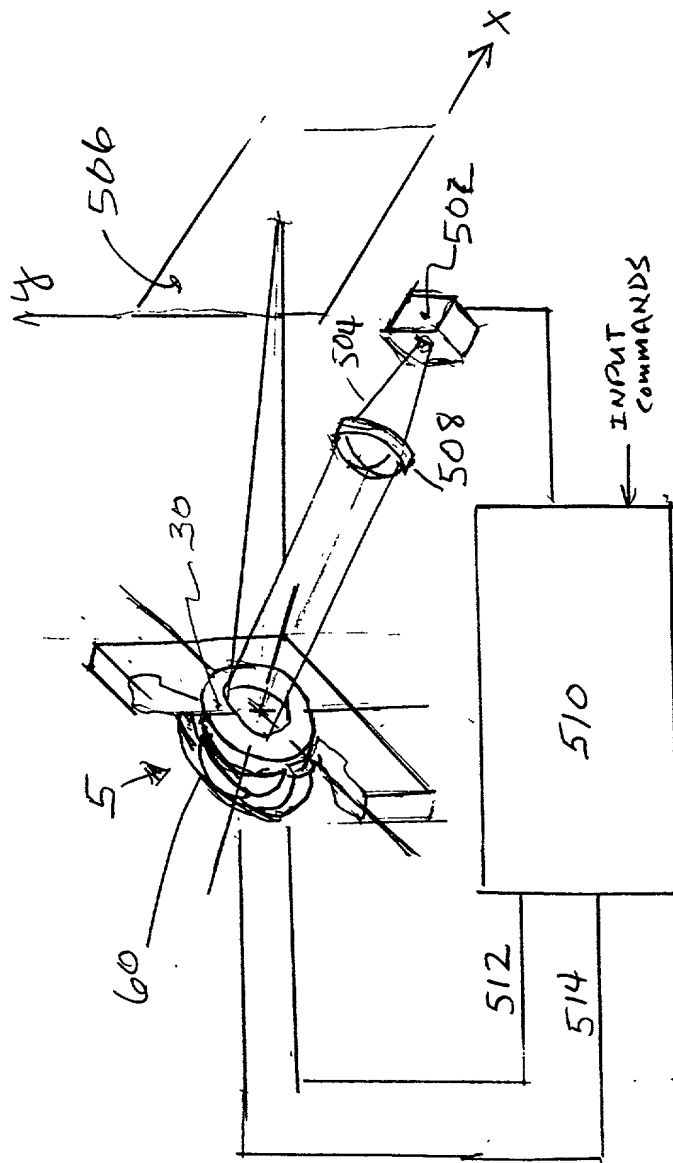


FIGURE 23

